

**REMARKS**

In response to the Office Action mailed April 11, 2005, in the above-captioned patent application, Applicants have canceled original claim 4, without prejudice to the subject matter claimed thereby, and have amended 1-3, 5, and 9. Thus, claims 1-3 and 5-12 currently are pending and are subject to examination in the above-captioned patent application. No new matter is added by the foregoing amendments, and these amendments are fully supported by the specification. See, e.g., Appl'n, Page 21, Lines 16-25; Page 22, Lines 1-9; Page 23, Lines 7-25; and Page 24, Lines 1-13. Applicants respectfully request that the Examiner reconsider the above-captioned patent application in view of the foregoing amendments and the following remarks.

In the Office Action mailed April 11, 2005, the Examiner rejected claims 1-12 under 35 U.S.C. § 102(e), as allegedly being anticipated by U.S. Patent No. 6,424,381 B1 to Mendenhall et al. ("Mendenhall"). Applicants respectfully traverse the Examiner's rejection, as follows.

Applicants have canceled original claim 4, without prejudice to the subject matter claimed thereby. Therefore, the rejection of claim 4 is rendered moot.

Applicants have amended independent claim 1 to describe an MPEG video decoder, comprising "an image decoding section which decodes parameters of each layer and a picture based on an MPEG bit stream, the image decoding section including an internal buffer for temporarily storing a decoded picture and decoded parameters; a frame memory having a plurality of banks and connected to the image decoding section, **wherein each of said banks has an area for storing the decoded picture and the decoded parameters** of each layer decoded by said image decoding section, and

**stores the decoded picture and the decoded parameters by mutually relating the decoded picture and the decoded parameters as a set**, wherein the layer includes a sequence layer which has a horizontal size value and a vertical size value, both expressing sizes of an image, as parameters; [and] a status register for storing values indicating whether display of the decoded picture stored in the frame memory has finished, **wherein the status register has an arbitration function for arbitrating between the decode control section and the display control section.**" Applicants' independent claim 9 includes corresponding method steps.

Thus, in Applicants' claimed invention as set forth in independent claims 1 and 9, the frame memory has a plurality of banks, each of the banks has an area for storing the decoded picture and the decoded parameters, and **each of the banks stores the decoded picture and the decoded parameters as a set**. Moreover, the status register has an arbitration function **for arbitrating between the decode control section and the display control section**.

In contrast to Applicants' claimed invention as set forth in independent claims 1 and 9, Mendenhall discloses a memory 114 having separate buffer areas. See, e.g., Mendenhall, Column 6, Lines 54-65. The memory 114 stores OSD (on-screen display) graphics data, audio and video data, MPEG system header channel data, SPU channel data, Navi Bank or private stream data. See, e.g., Id. at Column 7, Lines 49 to 53. Further, video decoder 130 also includes buffers that are used to store certain parameters that are useful for controlling the decoder 130. See, e.g., Id. at Lines 26-31. As such, the MPEG video decoder described in Mendenhall stores the video data and

the parameters in **different** memories. Therefore, Mendenhall fails to disclose or suggest storing the decoded picture and the decoded parameter as a **set**.

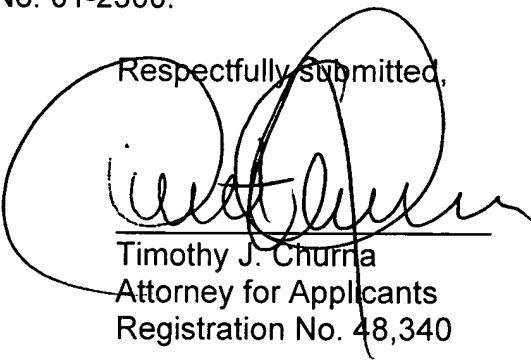
Mendenhall also fails to disclose or suggest a status register, as set forth in Applicants' independent claims 1 and 9. For example, in Mendenhall, the decoder microcontroller 128 controls arbitration to memory 114, and the arbitration priority favors MPEG video decoder 130 and channel interface 126. See, e.g., *Id.* at Lines 54-65. Thus, Mendenhall also fails to disclose or suggest arbitration between the decoder microcontroller 128 and the video decoder 130. Therefore, Applicants respectfully request that the Examiner withdraw the rejection of independent claims 1 and 9 at least for these reasons.

Claims 2, 3, 5-8, and 10-12 dependent from allowable independent claims 1 and 9, respectively. Therefore, Applicants respectfully request that the Examiner withdraw the rejection of claims 2, 3, 5-8, and 10-12 at least for this reason.

**CONCLUSION**

Applicants respectfully submit that the above-captioned patent application is in condition for allowance, and such action is earnestly solicited. If the Examiner believes that an in-person or telephonic interview with Applicants' representatives would expedite the prosecution of the above-captioned patent application, the Examiner is invited to contact the undersigned attorney of records. Applicants are filing a Petition For a One-Month Extension of Time with this response, and are enclosing a check in the amount of \$120.00 covering the requisite large entity fee for such a petition. Nevertheless, in the event of any variance between the fees determined by Applicants and those determined by the U.S. Patent and Trademark Office, please charge any such variance to the undersigned's Deposit Account No. 01-2300.

Respectfully Submitted,

  
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Enclosure: Petition for Extension of Time